Notice of Allowability	Application No.	Applicant(s)	
	10/605,010	HU ET AL.	
	Examiner	Art Unit	
	Tu-Tu Ho	2818	
The MAILING DATE of this communication appeals all claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT Roof the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate community (IGHTS). This application is s	this application. If not included in the control of	d course. THIS
1. This communication is responsive to Paper filed 08/23/200	<u>04</u> .	•	
2. The allowed claim(s) is/are <u>1-13</u> .			1.
3. The drawings filed on <u>01 September 2003</u> are accepted by	y the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority u a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority do 	e been received. e been received in Applicatio	n No	ion from the
International Bureau (PCT Rule 17.2(a)). * Certified copies not received:		a in the national stage applicati	
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		a reply complying with the req	uirements
5. A SUBSTITUTE OATH OR DECLARATION must be submiNFORMAL PATENT APPLICATION (PTO-152) which give			OTICE OF
 CORRECTED DRAWINGS (as "replacement sheets") mu (a) including changes required by the Notice of Draftsper 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner Paper No./Mail Date 	son's Patent Drawing Reviev		
Identifying indicia such as the application number (see 37 CFR feach sheet. Replacement sheet(s) should be labeled as such in			back) of
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT			ote the
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5. ☐ Notice of In	formal Patent Application (PTC)-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		ummary (PTO-413),	,
3. Information Disclosure Statements (PTO-1449 or PTO/SB/		Paper No./Mail Date 7. Examiner's Amendment/Comment	
Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit		Statement of Reasons for Allov	vance
of Biological Material	9.		
<u> </u>	David Nelms visory Patent Examiner hnology Center 2800		

DETAILED ACTION

Election/Restrictions

1. Claim 1 of Species I is generic and allowable. Accordingly, the restriction requirement between the two species is hereby withdrawn.

In view of the above noted withdrawal of the restriction requirement as to the linked species, applicant(s) are advised that if any claim(s) depending from or including all the limitations of the allowable generic linking claim(s) be presented in a continuation or divisional application, such claims may be subject to provisional statutory and/or nonstatutory double patenting rejections over the claims of the instant application. Once a restriction requirement is withdrawn, the provisions of 35 U.S.C. 121 are no longer applicable. See *In re Ziegler*, 44 F.2d 1211, 1215, 170 USPQ 129, 131-32 (CCPA 1971). See also MPEP § 804.01.

Allowable Subject Matter

2. Claims 1-13 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: The prior art of record fails to teach or render obvious a CMOS device having all exclusive limitations as recited in claims 1 and 10, comprising at least two thin-film transistors (TFT) of a first conductivity type and a second conductivity type, each TFT having a polysilicon island including a pair of source/drain regions (S/D) sandwiching a channel region, an inter-layer dielectric layer (ILD) having a plurality of contacts and covering the at least two TFTs, a conductive line formed on the ILD, and a plurality of S/D contact metals disposed on and through the ILD, the pair of S/D of one TFT of the at least two TFTs being arranged along the same direction as the pair of S/D of the other TFT or the at least two TFTs being arranged parallel to each other, and the conductive line being oriented in a direction perpendicular to the direction of the pair of S/D (the limitation

Art Unit: 2818

"the conductive line being oriented in a direction perpendicular to the direction of the pair of S/D" is the same as the conductive line being parallel to a gate line on either side of which is a source or drain) and electrically connecting a S/D of one TFT with a S/D of the other TFT.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

- 3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Copies of the references are not being furnished with this Office Action per MPEP § 707.05(a).
- a) U.S. Patent 6,001,714 to Nakajima et al. discloses an LCD driving circuit comprising a pair of CMOS TFTs where a channel length of one TFT is perpendicular to a channel length of the other TFT.
- b) U.S. Patent 6,590,228 to Voutsas et al. discloses an LCD device having a first set of TFTs and a second set of TFTs, the first set of TFTs having channel lengths perpendicular to those of the second set of TFTs, but all channel lengths are parallel to a predominant crystal orientation.

Application/Control Number: 10/605,010 Page 4

Art Unit: 2818

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tu-Tu Ho whose telephone number is (571) 272-1778. The examiner can normally be reached on 6:30 am - 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, DAVID NELMS can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tu-Tu Ho

Tu-Tu Ho September 09, 2004 Supervisory Patent Examiner Technology Center 2800